

# Quality Clause Q6R Revision Log

Last Revised: 11 October 2023

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<https://www.lockheedmartin.com/en-us/suppliers/business-area-procurement/aeronautics/quality-requirements/clauses.html>

REV	DATE	CHANGES
4	10-11-2023	Document reviewed for accuracy/updates – no changes
	9-27-2020	Document reviewed for accuracy/updates – no changes
	9-27-2017	Complete rewrite and reformat
3	5-01-2013	<p>ADDED:</p> <p><b>B.</b> Seller shall define and maintain an infrastructure to support its software development and testing, that includes, as applicable:</p> <ol style="list-style-type: none"><li>1. Software development tools and utilities, including host computer and support software;</li><li>2. Software verification tools and utilities, including test equipment, test software, vulnerability scanners, code scanners, and fuzzers;</li><li>3. Equipment, tools and utilities for archiving and storage, disaster recovery protection, replication, software loading, transmittal, record retention, software quality, change management, and configuration management;</li><li>4. Integrity verification tools and utilities (e.g., virus protection/checking, digital signatures, security hash algorithms, and CRC);</li><li>5. Equipment and software needed to meet retention requirements; and</li><li>6. Security for software environments against attacks (e.g., malicious code, enumeration, fingerprints, worms, viruses, backdoors, and spyware).</li></ol>

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2	6-16-2010	<p><b>REVISED:</b></p> <p><b>A.</b> Seller shall implement and maintain a Software Quality program or methodology acceptable to Buyer for engineering design, development, testing and delivery of standalone or embedded software products associated with this Purchase Order (“PO”). Buyer may accept Seller’s adaptation of standards SAE AS9115 for contracted deliverable software and SAE ARP 9005 for management of non-deliverable software if artifacts of compliance are readily verifiable, and the following elements, B, C, D, E, Note and Exclusion are defined and visible.</p> <p><b>B.</b> Seller shall implement, and comply with, its Software Quality Program Plan (“SQPP”) or Systems/Software Quality Program Plan (“SSQPP”), which shall be organized and documented in Seller’s format and defines Seller’s software and software quality planning. Seller’s SSQPP or SQPP is subject to Buyer approval or disapproval. Buyer reserves the right to conduct periodic evaluations of Seller’s compliance with the SQPP or SSQPP.</p> <p><b>C.</b> Seller shall include in its Software Quality program or methodology the review and audit of software development processes, products and activities, as required by Seller, in order to ensure compliance with applicable planning, procedures and standards. Seller shall provide internal and Buyer visibility into software quality program trends in its program.</p> <p><b>D.</b> Seller shall establish and maintain, as part of the Quality System required by this PO, a system which provides for evaluation of each non-deliverable software Item used in the automated manufacturing of deliverable hardware or in the qualification or acceptance of deliverable software or hardware. Seller shall ensure this system provides for the following:</p> <ol style="list-style-type: none"> <li>1. Required functions of the non-deliverable software are defined, in writing, and objective evidence exists prior to the intended use of such software, that such software performs the required functions.</li> <li>2. The non-deliverable software is placed under internal configuration control prior to its use and such internal configuration control is maintained throughout its use.</li> </ol> <p><b>ADDED:</b></p> <p><b>Note:</b> This Clause does apply to the development process for programmable devices used to store executable instructions or data operands which control the detailed operation of a microprocessor or integrated micro controller (commonly known as firmware). Quality Clause Q6R Revision: 2 16 June 2010 Page 2 of 2</p> <p><b>Exclusion:</b> This Clause does not apply to the development process for programmable logic elements of microcircuits that perform combinatorial or sequential logic (Boolean) functions which are developed using hardware development techniques. Such Items are subject to hardware development process requirements.</p>
Rev A	4-09-1999	Original Document